Mariam Mohammed Antar Mahdy

Junior Electronics and Communication Engineering Student

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Education

Ain Shams University, Faculty of Engineering

Bachelor of Engineering Sciences, Electronics and Communication   Class of 2025

- Currently in Junior Level
- Worth-mentioned Courses: Electronics – Computer Programming – Logic Design – Signals – Embedded Systems ARM – Digital Circuits - Electrical Measurements

El Abassia Secondary School for Girls

High School Degree (Thanwya Amma)   Class of 2020

- 393/410 (96%)

Technical Skills

• MS Office Suite	• Python	• MATLAB	• Data Visualization
• Quantitative Analysis	• C++	• Protous	• Octave
• C embedded	• Microcontroller	•Assembly	•Debugging skills

Faculty Projects

Logic Design   Fall 2021

- Collaborate with a team of 4 people to implement a binary/decimal "Full Adder" circuit using Protous software as circuit designer and build it on a breadboard using simple gates and electronics components, Get an A+ in the project.

Thermal Power Engineering   Spring 2022

- Built an electromechanical system to lift a coin using the power of tea candle, by Electrothermal generator and dc motor to lift the coin, Get an A+ in the project.

Electrical Measurements   Spring 2022

- Create a Parking lot systems to detect the empty and filled parking spaces, Using Arduino uno and IR sensors. Get an A+ in the project.

Electronics   Fall 2022

- Built an "Operation Amplifier Electronics System" to get a sound signal from phone or pc and apply electronic operations on it to amplifier the audio signal.
- Customizing an AC to DC Power Supply with 220V Input with Bridge Rectifier, Zener Diode, and Voltage Regulator (Get B+ in the Project)

Embedded Systems   Spring 2023

- GPS Tracker System, using Tiva C that tracks the location of a vehicle using GPS and displays the location on a web interface. The system is controlled by a Tiva C microcontroller and uses GPS and GSM modules to track and transmit location data.

Digital Circuits   Spring 2023

- Design ALU can execute arithmetic and logical operations using Verilog, the output is selected by the MSB of the selection line, while the required operation is selected by the other 3 bits.